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BIB DATA SHEET

CONFIRMATION NO. 3051

SERIAL NUM					GROUP ART UNIT		ATTORNEY DOCKET NO.			
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APPLICANTS Kazuaki Goto, Miyazaki, JAPAN; Reiko Harada, Miyazaki, JAPAN;										
** CONTINUING DATA **********************************										
** FOREIGN APPLICATIONS ************************************										
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/07/2004										
Foreign Priority claimed Yes V No 35 USC 119(a-d) conditions met Yes V No			Met after Allowance		STATE OR COUNTRY	SHEETS DRAWINGS	TOT CLAI		INDEPENDENT CLAIMS	
F	'HELEN ROSSOSH Examiner's		Initials		JAPAN	6	1, <u>F</u>	<u>.</u>	3· 2	
ADDRESS										
Studebaker & Brackett PC One Fountain Square 11911 Freedom Drive, Suite 750 Reston, VA 20190 UNITED STATES										
TITLE										
Method of designing a circuit layout of a semiconductor device										
							☐ All Fees ☐ 1.16 Fees (Filing)			
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following: 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue)							ing Ext. of time)		
							1 Other			
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